

a storage area for storing the information that characterize a product term, named mask word and product word;

first AND gates means for receiving said inputs and said mask word to produce a first intermediate result, which identify the input variables that form a product term;

EQUIVALENCE gates means for comparing the said product term with said first intermediate result to produce a second intermediate result concerning a product term;

second AND gate means for receiving the said second intermediate result to produce a logical value which is the value of the product term.

REMARKS

New claim 12 defines an embodiment where $r = 1$, and therefore there is not a need to use the "function word" register.

Respectfully submitted.

Ioan Dancea

